

ABSTRACT

Reproduction signals which are simultaneously
obtained from an Ach head 3 and a Bch head 4 are time division
5 multiplexed by a signal layout converting circuit 9 and
arranged. An HPF unit 12 and an LPF unit 14 of a waveform
equalizing circuit 16 are optimally switched on the basis
of sync adjustment information such as switching information
of the heads. An edge detection pulse width of an edge
10 detecting circuit and an output frequency of a VCO 23 are
controlled on the basis of the sync adjustment information.
The output frequency of the VCO 23 is used as a clock signal
of the reproduction signals by a signal processing unit 27
at the post stage.